WEST Search History

DATE: Monday, January 27, 2003

Set Name side by side	Query	Hit Count	Set Name result set
DB=JP.	AB; PLUR=YES; OP=ADJ		
L10	ferroelectric memory and analog adj2 signal\$1	0	L10
DB=US	SPT,PGPB; PLUR=YES; OP=ADJ		
L9	L8 and test\$1 mode	2	L9
L8	14 and analog adj2 signal\$1	17	L8
DB=JP	AB; PLUR=YES; OP=ADJ		
L7	ferroelectric and memory and analog adj2 amplifier\$1	0	L7
DB=US	SPT,PGPB; PLUR=YES; OP=ADJ		
L6	L4 and analog adj2 amplifier\$1	14	L6
L5	L4 and analog adj2 amplfier\$1	0	L5
L4	L3 and test\$	501	L4
L3	L2 and cell\$1	2021	L3
L2	ferroelectric adj3 memory	2586	L2
L1	ferrolectric adj3 memory	6	L1

END OF SEARCH HISTORY

WEST

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Search Results - Record(s) 1 through 10 of 17 returned.

1. Document ID: US 20030002368 A1

L8: Entry 1 of 17

File: PGPB

Jan 2, 2003

PGPUB-DOCUMENT-NUMBER: 20030002368

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030002368 A1

TITLE: Circuit for testing ferroelectric capacitor in fram

PUBLICATION-DATE: January 2, 2003

INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY RULE-47

Kang, Hee Bok Daejeon-shi KR
Kye, Hun Woo Kyonggi-do KR
Kim, Duck Ju Cheju-do KR
Park, Je Hoon Kyonggi-do KR
Lee, Geun Il Kyonggi-do KR

US-CL-CURRENT: 365/201

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC
Drawd D	esc Ir	nage									

2. Document ID: US 20020160711 A1

L8: Entry 2 of 17

File: PGPB

Oct 31, 2002

PGPUB-DOCUMENT-NUMBER: 20020160711

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020160711 A1

TITLE: Imager integrated CMOS circuit chip and associated optical code reading

systems

PUBLICATION-DATE: October 31, 2002

INVENTOR-INFORMATION:

NAME CITY STATE COUNTRY RULE-47

Carlson, Bradley S. Northport NY US
Bridgelall, Raj Mount Sinai NY US

US-CL-CURRENT: 455/41; 455/66

3. Document ID: US 20020159315 A1

L8: Entry 3 of 17

File: PGPB

Oct 31, 2002

PGPUB-DOCUMENT-NUMBER: 20020159315

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020159315 A1

TITLE: Semiconductor memory

PUBLICATION-DATE: October 31, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE COUNTRY

RULE-47

Noguchi, Mitsuhiro

Yokohama-Shi

JP JP

Goda, Akira Matsunaga, Yasuhiro Yokohama-Shi Kawasaki-Shi

JP

US-CL-CURRENT: 365/200

Full Tit	e Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Drawi Desc	Image	<u> </u>								

4. Document ID: US 20020113763 A1

L8: Entry 4 of 17

File: PGPB

Aug 22, 2002

PGPUB-DOCUMENT-NUMBER: 20020113763

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020113763 A1

TITLE: Liquid crystal display device and method of driving the same

PUBLICATION-DATE: August 22, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Koyama, Jun

Kanagawa

JΡ

US-CL-CURRENT: 345/87

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWC |
Draws Desc Image

5. Document ID: US 20020097619 A1

L8: Entry 5 of 17

File: PGPB

Jul 25, 2002

PGPUB-DOCUMENT-NUMBER: 20020097619

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020097619 A1

TITLE: Test circuit for an analog measurement of bit line signals of ferroelectric

memory cells

PUBLICATION-DATE: July 25, 2002

INVENTOR - INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Jacob, Michael

Munchen

DE

Rohr, Thomas

Yokohama City

JP

US-CL-CURRENT: 365/201; 365/200

Draw Desc Image

Full Title Citation Front Review Classification Date Reference Sequences Attachments

6. Document ID: US 20020093847 A1

L8: Entry 6 of 17

File: PGPB

Jul 18, 2002

PGPUB-DOCUMENT-NUMBER: 20020093847

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020093847 A1

TITLE: Ferroelectric storage device

PUBLICATION-DATE: July 18, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Tada, Yoshihiro

Ukyo-ku

JP

US-CL-CURRENT: 365/145

Drawl Desc Image

Full Title Citation Front Review Classification Date Reference Sequences Attachments

7. Document ID: US 20020036920 A1

L8: Entry 7 of 17

File: PGPB

Mar 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020036920

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020036920 A1

TITLE: Array architecture and operating methods for digital multilevel nonvolatile

memory integrated circuit system

PUBLICATION-DATE: March 28, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

OUNTRY

RULE-47

Tran, Hieu Van

San Jose

Khan, Sakhawat M.

Sunnyvale

CA

US

Korsh, George J.

Redwood City

CA CA

US US

US-CL-CURRENT: 365/185.03

Full Title Citation Front Review Classification Date Reference Sequences Attachments

KWIC

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8. Document ID: US 20020024485 A1

L8: Entry 8 of 17

File: PGPB

Feb 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020024485

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020024485 A1

TITLE: Liquid crystal display device and driving method thereof

PUBLICATION-DATE: February 28, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Koyama, Jun

Kanaqawa

JΡ

US-CL-CURRENT: 345/87

Full Title Citation Front Review Classification Date Reference Sequences Attachments Drawi Desc | Image |

KWIC

9. Document ID: US 20020021295 A1

L8: Entry 9 of 17

File: PGPB

Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020021295

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020021295 A1

TITLE: Liquid crystal display device and method of driving the same

PUBLICATION-DATE: February 21, 2002

INVENTOR - INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Koyama, Jun

Kanagawa

JP

Yamazaki, Shunpei

Tokyo

JP

US-CL-CURRENT: 345/205; 345/87

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

10. Document ID: US 20020021274 A1

L8: Entry 10 of 17

File: PGPB

Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020021274

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020021274 A1

TITLE: Liquid crystal display device, method of driving the same, and method of driving a portable information device having the liquid crystal display device

PUBLICATION-DATE: February 21, 2002

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Koyama, Jun

Kanagawa

JP

Yamazaki, Shunpei

Tokyo

JP

US-CL-CURRENT: 345/98

Full Title Citation Front Review Classification Date Reference Sequences Attachments

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Term	Documents
ANALOG.USPT,PGPB.	251955
ANALOGS.USPT,PGPB.	47199
ANALOGUE.USPT,PGPB.	31874
ANALOGUES.USPT,PGPB.	26182
SIGNAL\$1	0
SIGNAL.USPT,PGPB.	942240
SIGNALA.USPT,PGPB.	56
SIGNALB.USPT,PGPB.	13
SIGNALC.USPT,PGPB.	4
SIGNALD.USPT,PGPB.	21
SIGNALE.USPT,PGPB.	101
(L4 AND ANALOG ADJ2 SIGNAL\$1).USPT,PGPB.	17

There are more results than shown above. Click here to view the entire set.

Display Format: ___

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Search Results - Record(s) 11 through 17 of 17 returned.

11. Document ID: US 20010055229 A1

L8: Entry 11 of 17

File: PGPB

Dec 27, 2001

PGPUB-DOCUMENT-NUMBER: 20010055229

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010055229 A1

TITLE: Semiconductor memory device and testing system and testing method

PUBLICATION-DATE: December 27, 2001

INVENTOR - INFORMATION:

NAME

CITY

STATE

COUNTRY

RULE-47

Koike, Hiroki

Tokyo

JP

US-CL-CURRENT: 365/200

Full Title Citation Front Review Classification Date Reference Sequences Attachments

12. Document ID: US 20010051766 A1

L8: Entry 12 of 17

File: PGPB

Dec 13, 2001

KWIC

PGPUB-DOCUMENT-NUMBER: 20010051766

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010051766 A1

TITLE: Endoscopic smart probe and method

PUBLICATION-DATE: December 13, 2001

INVENTOR - INFORMATION:

NAME

CITY

STATE

COUNTRY RULE-47

Gazdzinski, Robert F.

San Diego

CA

US

US-CL-CURRENT: 600/309; 128/903, 378/119, 600/300, 600/437, 600/562, 604/65, 606/1, 606/32, 607/92, 623/23.64

Full Title Citation Front Review Classification Date Reference Sequences Attachments Draws Desc Image

13. Document ID: US 6504754 B2

L8: Entry 13 of 17 File: USPT Jan 7, 2003

US-PAT-NO: 6504754

DOCUMENT-IDENTIFIER: US 6504754 B2

TITLE: Array architecture and operating methods for digital multilevel nonvolatile

memory integrated circuit system

DATE-ISSUED: January 7, 2003

INVENTOR - INFORMATION:

NAME CITY

STATE ZIP CODE

COUNTRY

Tran; Hieu Van

San Jose

CA

Khan; Sakhawat M.

Sunnyvale

CA

Korsh; George J.

Redwood City

CA

US-CL-CURRENT: 365/185.03; 365/185.18, 365/185.19



KMC

14. Document ID: US 6480979 B1

L8: Entry 14 of 17

File: USPT

Nov 12, 2002

US-PAT-NO: 6480979

DOCUMENT-IDENTIFIER: US 6480979 B1

TITLE: Semiconductor integrated circuits and efficient parallel test methods

DATE-ISSUED: November 12, 2002

INVENTOR - INFORMATION:

NAME

CITY

STATE ZIP CODE

COUNTRY

Tomari; Nobuhiro

Miyazaki

JР

US-CL-CURRENT: 714/724; 714/718

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw Desc Image

KWIC

15. Document ID: US 6200265 B1

L8: Entry 15 of 17

File: USPT

Mar 13, 2001

US-PAT-NO: 6200265

DOCUMENT-IDENTIFIER: US 6200265 B1

TITLE: Peripheral memory patch and access method for use with an implantable medical

device

DATE-ISSUED: March 13, 2001

INVENTOR-INFORMATION:

NAME

CITY

STATE

A 7.

MN

ZIP CODE

COUNTRY

. Walsh: Kevin K.

Peoria

Thompson; David L. Fridley

US-CL-CURRENT: 600/300; 128/903

Full Title Citation Front Review Classification Date Reference Sequences Attachments

Draw, Desc Image

☐ 16. Document ID: US 5903253 A

L8: Entry 16 of 17

File: USPT

May 11, 1999

US-PAT-NO: 5903253

DOCUMENT-IDENTIFIER: US 5903253 A

TITLE: Image data control apparatus and display system

DATE-ISSUED: May 11, 1999

INVENTOR - INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Mizutome; Atsushi

Fujisawa

JΡ

Yuki; Osamu Inoue; Hiroshi Atsuqi Yokohama JΡ JΡ

US-CL-CURRENT: 345/694; 345/698

Full Title Citation Front Review Classification Date Reference Sequences Attachments Draw Desc Image

KWIC

☐ 17. Document ID: US 5254482 A

L8: Entry 17 of 17

File: USPT

Oct 19, 1993

US-PAT-NO: 5254482

DOCUMENT-IDENTIFIER: US 5254482 A

TITLE: Ferroelectric capacitor test structure for chip die

DATE-ISSUED: October 19, 1993

INVENTOR - INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Fisch; David E.

Sunnyvale

CA

US-CL-CURRENT: 438/3; 257/48, 365/144, 365/145, 365/149, 365/201, 438/18

Full Title Citation Front Review Classification Date Reference Sequences Attachments

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